

BEST AVAILABLE COPY**PATENT NUMBER and
ISSUE DATE****U.S. UTILITY Patent Application**

APPL NUM 10032761	FILING DATE 12/27/2001	CLASS 324	SUBCLASS 765	GAU <i>2829</i>	EXAMINER <i>Jimmy N</i>
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**APPLICANTS: Takechi Keizo; Ohsaki Akio; Hayashi Yoshihiko; Murata Kazuhiko;

*2829*****CONTINUING DATA VERIFIED:***None***** FOREIGN APPLICATIONS VERIFIED:**

JAPAN 2001-150853 05/21/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO 16869N-039000US
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	
TITLE : Driver circuit integrated with load current output circuit, pin electronics and IC tester having thereof			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436U(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
TERMINAL DISCLAIMER		Application Examiner		
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